

Notice of Allowability

Application No.

10/699,709

Examiner

Tuyen To

Applicant(s)

SIGNORE ET AL.

Art Unit

2825

TT

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 01/06/2006.
2. ☒ The allowed claim(s) is/are 1-22.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).


* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 12/22/2003
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


VUTHE SIEK
PRIMARY EXAMINER

DETAILED ACTION

This is a response to the amendment and remarks/arguments filed on 01/06/2006.

Applicants' remarks/arguments filed 01/06/2006 are considered persuasive and obviates all claim rejections. Accordingly, claims 1-22 are allowed.

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.
2. Authorization for this examiner's amendment was given in a telephone interview with Hoyt A. Fleming III (Reg. No. 41,752) on 03/29/2006.
3. The application has been amended as follows:

In the claims

4. **Claim 1, line 1**, after "determining" **delete** " the load of the gates driven by a logic gate divided by the size of the logic gate, i.e., the fanout, for at least one transistor" **insert** -- a fanout, wherein the fanout is the load of the gates driven by a logic gate divided by the size of at least one transistor in the logic gate --.
5. **Claim 1, line 5**, after "includes" **delete** " a resistor that dynamically varies based upon one or more values in the sizing model, i.e., a dynamic resistor" **insert** -- a dynamic resistor, wherein the dynamic resistor is a resistor that dynamically varies based upon one or more values in the sizing model --.

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6. **Claim 1, line 8**, after "the size of " insert -- the --.
7. **Claim 1, line 9**, after "the fanout for" insert --the--.
8. **Claim 10, line 5**, after "includes" **delete** " a resistor that dynamically varies based upon one or more values in the sizing model, i.e., a dynamic resistor" **insert** – a dynamic resistor, wherein the dynamic resistor is a resistor that dynamically varies based upon one or more values in the sizing model --.
9. **Claim 19, line 5**, after "includes" **delete** " a resistor that dynamically varies based upon one or more values in the sizing model, i.e., a dynamic resistor" **insert** – a dynamic resistor, wherein the dynamic resistor is a resistor that dynamically varies based upon one or more values in the sizing model --.

Allowable Subject Matter

10. **Claims 1-22** contain allowable subject matter.
11. The following is a statement of reasons for the indication of allowable subject matter:

Claims 1-22 are allowed because *the prior art of record does not teach or fairly suggest* a method (or a device/ an integrated circuit) of determining a fanout of a logic gate comprising the step of creating a sizing model by replacing at least one logic element in a circuit description with a sizing element that includes a dynamic resistor, wherein the dynamic resistor is a resistor that dynamically varies based upon one or more values in the sizing model *in combination with the other claim limitations*.

12. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion


13. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuyen To whose telephone number is (571) 272-8319. The examiner can normally be reached on 9:00am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Chiang can be reached on (571) 272-7483. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

14. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


Tuyen To

Patent examiner


VUTHE SIEK
PRIMARY EXAMINER